

Serial No. 09/712,145
Amdt. dated November 9, 2004
Reply to Office Action of June 6, 2004

Docket No. K-0234

Amendments to the Abstract:

Please replace the original Abstract with the revised Abstract attached at the end of this paper.

ABSTRACT

~~Apparatus~~ An apparatus for generating pseudo-noise codes adapted for advancing or retarding ~~[[1]]~~ pseudo-noise ~~chip chips~~ generated therefrom within one clock in a radio communication network base on ~~[[the]]~~ code division multiple access (CDMA), ~~including the.~~ The apparatus includes a control unit for outputting a control signal for the normal state or a PN chip advance; a plurality of MUXs for outputting an output value of the next state for a normal operation or an output value of a PN chip advance as an output signal in response to the control signal of ~~said the~~ control unit; and a plurality of shift registers ~~connected~~ for outputting PN chip codes of the next or the second next state during one system clock time period in response to said MUXs~~[[,]]~~ Further, the input end of each of ~~said the~~ shift registers ~~being~~ is connected to the output end of each of ~~said~~ MUX.